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AP179HO



**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Viresh Ratnakar, et al.

Group Art Unit:

2643

Serial No.:

10/631,155

Examiner:

Wing F. Chan

Filed:

July 31, 2003

Title:

Video Codec System with Real-Time Complexity Adaptation

### **CERTIFICATE OF MAILING**

I hereby certify that this Information Disclosure Statement and the documents attached hereto, are being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: April 19, 2006

<sup>J</sup> Virginia Silva

# SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the document(s) listed on the enclosed Form PTO-1449. A copy of each listed document is enclosed, except, in the case where this application is filed after June 30, 2003, copies of any U.S. patents and U.S. patent application publications are not enclosed.

This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits. No statement or fee is required (37 CFR §1.97(b)).

# CONCLUSION

The Commissioner is hereby authorized to charge any additional fees, which may be required, or credit any over-payment to Deposit Account No.: 19-2746.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Respectfully submitted.

Daniel A. Ratoff

Registration No. 54,389

Please address all correspondence to:

Epson Research and Development, Inc. Intellectual Property Department 150 River Oaks Parkway, Suite 225 San Jose, CA 95134 Phone: (408) 952-6000

Facsimile: (408) 954-9058 Customer No. 20178

Date: April 19, 2006

# DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION

**EXAMINER** 

SERIAL NO.
10/631,155

ATTY DOCKET NO. AP179HO

APPLICANT

Viresh Ratnakar, et al.

VERAL SHEETS IF NECESSARY) FILING DATE

GROUP

PAGE 1 OF 1 July 31, 2003 2643 U.S. PATENT DOCUMENTS FILING E.I. DOCUMENT NUMBER DATE NAME CLASS **SUBCLASS** DATE 2002/0025001 2/28/02 Ismaeil, et al. AAAB ACADΑE AF AG AH ΑI ΑJ  $\mathbf{AK}$ FOREIGN PATENT DOCUMENTS TRANSLATION E.I. DOCUMENT NUMBER DATE COUNTRY SUBCLASS CLASS AL AMAN AO APAQOTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) RICHARDSON I E G: "Video codec design", John Wiley & Sons, LTD., 1 January 2002, pp. 226-233. paragraph [10.3] - paragraph [10.4]. AR LENGWEHASATIT, K., et al., "A Novel Computationally Scalable Algorithm for Motion Estimation", Proceedings of the SPIE, SPIE, Bellingham, VA, vol. 3309, no. 1, January 28, 1998, pp. 68-79. AS AT

DATE CONSIDERED